



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Minghao (Mary) Zhang, et al
Title: Center-tap transformers in integrated circuits
Serial No.: 10/624,308
Filing Date: 07/22/2003
Examiner: Jennifer A. Poker
Group Art Unit: 2832
Docket No.: Qantec-07

November 27, 2004

Mail Stop: IDS
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.56 and 1.97(b)

Dear Sir:

The references listed in the attached PTO Form 1449, a copy of which is attached, may be material to examination of the above-identified patent application. The submission herewith does not constitute an admission of the references as the prior art or an exhaustive search performed in the art. Items A – F are US patent publications and have been electronically submitted November 15, 2004. Items N, O and P are three new items, a copy of which is enclosed herewith.

The applicant submits these references in compliance with their duty of disclosure pursuant to 37 CFR 1.56 and 1.97. It is respectfully requested that the cited references be considered and that the enclosed copy of Information Disclosure Statement be initialed by the Examiner to indicate such consideration and a copy thereof returned to the applicant.

This Information Disclosure Statement is believed to be filed before the mailing date of a first Office Action on the merits. Accordingly, the applicants believe that there are no fees due in connection with the filing of this Information Disclosure

Statement. However, if the PTO believes otherwise please charge our deposit account No. 50-2436 any fees that may be due. Please telephone the undersigned at (408)777-8873, if there are any questions.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to " Mail Stop: IDS, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450", on November 27, 2004.

Name: Joe Zheng

Signature: 

Respectfully submitted;



Joe Zheng
Reg.: No. 39,450

Form PTO-1449
(Modified)

DEC 02 2004

Docket No.: Attn: Qantec-07

Application No.: 10/624,308

List of Patents and Publications Statement
(Use several sheets if necessary)

Applicant: Minghao (mary) Zhang, et al

Filing Date: 07/22/2003

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

Examiner Initials	Document No.	Date	Patentee	Class	Sub-Class	Filing date if appropriate
	A. US 6,188,306	Feb. 13, 2001	Wollesen	336/200		Dec. 5, 1997
	B. US 5,892,425	Apr. 6, 1999	Huhn et al	336/200		Apr. 10, 1997
	C. US 6,380,835	Apr. 30, 2002	Lee	336/200		Apr. 25, 2000
	D. US 5,572,179	Nov. 05, 1996	Ito	336/200		Jan. 10, 1995
	E. 20020105406	Aug. 8, 2002	Liu	336/200		Feb. 8, 2001
	F. 20030030497	Feb. 13, 2003	Duncan	331/34		Jun. 17, 2002
	G.					

FOREIGN PATENT DOCUMENTS

No.		Document No.	Date	Country	Class	Sub-Class	Trans-lation
	H.						
	I.						
	J.						
	K.						
	L.						
	M.						

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

N.	John Long "Monolithic Transformers for Silicon RF IC Design", IEEE Journal of solid-state circuits, Vol. 35, No. 9, September 2000.
O.	Alireza Zolfaghari et al "Stacked inductors and transformers in CMOS technology", IEEE Journal of solid-state circuits, Vol. 36, No. 4, April 2001
P	S. Mohan "Modeling and Characterization of on-chip transformers", the publication date and source are unknown, the publication is believed to be earlier than the filing date of this application

Examiner

Date Considered

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.